



EAST - [Untitled1:1]

File View Edit Tools Window Help

Drafts

Pending

Active

L1: (84) data same sour

L2: (42) l1 and (transi

L3: (4) l1 same (transi

Failed

Saved

Favorites

Tagged (0)

UDC

Queue

Trash

Search

List

Browse

Queue

Clear

DBs

USPAT

Default operator:

OR

Parale

Highlight all hit terms initially

l1 same (transfer\$4 near3 complete\$1)

BRS form

IS&R form

Image

Text

HTML

	U	1	Document ID	Issue Dat	Pages	Title	Current OR	Current XR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6981073	20051227	42	Multiple channel data	710/28	710/14;
			B2			bus control for video v		710/27;
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5668815	19970916	22	Method for testing	714/719	714/720
			A			integrated memory using		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 5278968	19940111	10	Microprocessor capable	712/42	
			A			of transferring data wi		
4	<input type="checkbox"/>	<input type="checkbox"/>	US 3766526	19731016	16	MULTI-MICROPROGRAMMED	710/51	
			A			INPUT-OUTPUT PROCESSOR		